

計算機概要

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圖1

S	R	Q	Q'	
1	0	1	0	
0	0	1	0	(after S = 1, R = 0)
0	1	0	1	
0	0	0	1	(after S = 0, R = 1)
1	1	0	0	

(b) Truth table

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2., 如下:

SR = 01, Q = 1

SR = 10, Q = 0

SR = **11**, Q = 前一次的狀態

SR = **00**, Q = 1, Q' = 1